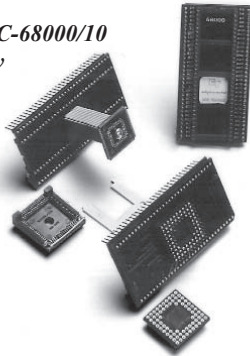


EMULATOR ADAPTERS - DEVICE-SPECIFIC - SOCKETABLE

AS-DIP-LCC-68000/10
Bottom View



Top View

AS-DIP-PCC-68000
Bottom View

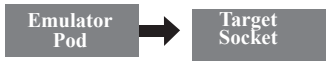
AS-DIP-PGA2-68000/10
Bottom View

Ordering Information Example

Product Code _____ AS-PGA-QF03T-68302 _____ IC to be Emulated
 Emulator Pod Female Receptacle _____ Footprint Target Socket

T = For Textool Socket
 Y = For Yamaichi Socket

For a complete listing of emulator adapters pricing and delivery information, see:
 Web Link: www.1800adapter.com/093



Icons identify your emulator pod and target socket.

EMULATOR TOOLS & ADAPTERS

Motorola	Motorola	AMD
<p>DIP → PGA</p> <p>Description # Document # AS-DIP-PGA2-68000/10 F2230 AS-DIP-PGA2-68000/10LN F2432</p> <p>LN = Low Noise Version with ground plane and no test points. (PGA2 = 10 X 10)</p> <p>DIP → PLCC</p> <p>Description # Document # AS-DIP-PCC-68000/10 F1234 AS-DIP68K-PCC-68EC000 F1155 AS-DIP-PCC-68HC11 F1982 AS-DIP-PCC-68008 F2290</p> <p>(Note: Used with 48 pin or 52 pin emulator pod) AS-DIP-PCC-6809 F2289</p> <p>PGA → DIP</p> <p>Description # Document # AS-PGA2-DIP-68000/10 F2474 AS-PGA2-DIP-68000/10LN F1805</p> <p>LN = Low Noise Version with ground plane and no test points.</p>	<p>PLCC → QFP</p> <p>Description # Document # AS-PCC5-QF14Y-68HC11K4 F1969</p> <p>PLCC → DIP</p> <p>Description # Document # AS-PCC-DIP-68000/10 F2465 AS-PCC-DIP-68HC11 F1385 AS-PCC-DIP-68008 F2229</p> <p>(Note: Used with 48 pin or 52 pin DIP target board)</p> <p>PLCC → PGA</p> <p>Description # Document # AS-PCC-PGA2-68000/10 F2070 AS-PCC1S-PGA2-MC68440 F1687</p> <p>PLCC → PLCC</p> <p>Description # Document # AS-PCC68K-PCC-68EC000 F1097 AS-PCC68EC-PCC1-68HC000/68000 F2470</p>	<p>PGA → PQFP</p> <p>Description # Document # AS-PGA8-QF03T-386DXL F1400</p>
		<p>Microchip</p> <p>DIP → PLCC</p> <p>Description # Document # AS-DIP-PCC-PIC16C74/64/65 F3939 AS-DIP-PCC-PIC17C42/43/44 F3970</p>
		<p>Wired 1 to 1</p> <p>PLCC → QFP</p> <p>Description # Document # AS-PCC3-QF16S-GEN F1377 AS-PCC3-QF16DS-GEN F3949 AS-PCC5-QF02S-GEN F4327</p> <p>QFP → QFP</p> <p>Description # Document # AS-QF08S-QF08S-GEN F4498 AS-QF09S-QF09S-GEN F5004 AS-QF10S-QF63Y-GEN F5078</p>
	<p>Hitachi</p> <p>SHRINK DIP → PLCC</p> <p>Description # Document # AS-SDIP-PCC-64180 F1023 AS-SDIP-PCC-6301/701 F1024</p> <p>PLCC → SHRINK DIP</p> <p>Description # Document # AS-PCC-SDIP-64180 F2467</p> <p>PLCC → QFP</p> <p>Description # Document # AS-PCC5-QF47Y-H8534 F4559</p> <p>PGA → SHRINK DIP</p> <p>Description # Document # AS-PGA-SDIP-64180 F2472</p>	
	<p>Intel</p> <p>PLCC → PLCC</p> <p>Description # Document # AS-PCC1-PCC4-N80C196KB/198 F1278 AS-PCC1-PCC5-80186EB F1298</p> <p>PGA → LCC</p> <p>Description # Document # AP3-68-PGA F2374 (Used with Intel 80186/80286)</p>	
<p>Zilog</p> <p>DIP → PLCC</p> <p>Description # Document # AS-DIP-PCC-Z80 F2058 AS-DIP-PCC-Z86C21 F4547</p>		<p>Intel</p> <p>DIP → PLCC</p> <p>Description # Document # AS-DIP-PCC-8031/51 F3789 AS-DIP-PCC-8031/51VP F3789 AS-DIP-PCC-8031/51Z F1520 AS-DIP-PCC-8086/88 F3790</p> <p>LCC → PLCC</p> <p>Description # Document # AP4-68-LCC F1077 (Used with Intel 80186/80286)</p> <p>PGA → PLCC</p> <p>Description # Document # AP4-68-PGA F1124 (Used with Intel 80186/80286) AP4-68-PGA1-FLIP* F1838 (Used with Intel MCS96)</p> <p>PLCC → DIP</p> <p>Description # Document # AS-PCC-DIP-8031/51 F1137</p>